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				First Named Inventor	Everett Lee	
-				Art Unit		
			,	Examiner Name		
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NON PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, acrial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T'				
Pr		KIM, J., et al., "A Novel 4.6F2 NOR Cell Technology With Lightly Doped Sourd (LDS) Junction For High Density Flash Memories," IEEE, 1998, 4 pages,	:				
H		Memory Division, Samsung Electronics Co., LTD.	:				
Pr		PAVAN, P., et al., "Flash Memory Cells-An Overview," IEEE, 1997, Vol. 85, No. 8, August 1997, pages 1248-1271.	:				
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